

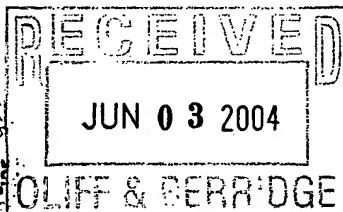


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|------------|---------------------------|----------|---------------|----------------|----------|----------|----------|
| 10/801,547 | 03/17/2004                | 2183     | 770           | 119102         | 7        | 11       | 2        |

25944  
 OLIFF & BERRIDGE, PLLC  
 P.O. BOX 19928  
 ALEXANDRIA, VA 22320



OLIFF & BERRIDGE

CONFIRMATION NO. 2324

## FILING RECEIPT



\*OC000000012844514\*

Date Mailed: 06/02/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

Masakazu Isomura, Suwa-shi, JAPAN;

setagaya-RU

## Assignment For Published Patent Application

SEIKO EPSON CORPORATION, Tokyo, JAPAN;

## Domestic Priority data as claimed by applicant

## Foreign Applications

JAPAN 2003-092371 03/28/2003

If Required, Foreign Filing License Granted: 06/01/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

## Title

Vector processor and register addressing method



**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Masakazu ISOMURA

Application No.: 10/801,547

Docket No.: 119102

Filed: March 17, 2004

For: VECTOR PROCESSOR AND REGISTER ADDRESSING METHOD

**REQUEST FOR CORRECTION OF PALM RECORDS**

Commissioner for Patents  
P.O. Box 1450  
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Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records. A Supplemental Application Data Sheet is attached.

Respectfully submitted,

  
James A. Oliff  
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Thomas J. Pardini  
Registration No. 30,411

JAO:TJP/mlo

Date: July 29, 2004

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# OLIFF & BERRIDGE, PLC

ATTORNEYS AT LAW

## Supplemental Application Data Sheet

### **Application Information**

Application Type:: Regular  
Subject Matter:: Utility  
CD-ROM or CD-R:: None  
Title:: VECTOR PROCESSOR AND REGISTER  
ADDRESSING METHOD  
Attorney Docket Number:: 119102  
Suggested Drawing Figure::  
Total Drawing Sheets:: 7  
Small Entity:: No

### **Applicant Information**

Applicant Authority type:: Inventor  
Primary Citizenship Country:: Japan  
Status:: Full Capacity  
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Country of Residence:: JAPAN

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Correspondence Customer Number:: 25944

| <b>Foreign Priority Information</b>     |  |               |                    |
|---|--|---------------|--------------------|
| Country::                               | Application Number::                     | Filing Date:: | Priority Claimed:: |
| JAPAN                                   | 2003-092371                              | 3/28/2003     | Yes                |
| <b>Assignee Information</b>             |  |               |                    |
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| Postal or Zip Code of mailing address:: | 163-0811                                 |               |                    |